

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 7,062,664 B2  
APPLICATION NO. : 09/917833  
DATED : June 13, 2006  
INVENTOR(S) : Atsushi Date et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

COLUMN 6:

Line 38, "PCi" should read -- PCI --.

COLUMN 14:

Line 1, "and 803" should read -- and 1ru 803 --.

COLUMN 16:

Line 30, "SEB" should read -- SBB --.

COLUMN 18:

Line 26 to 26, "bWBurstReq L," should read -- bWBurstReq\_L, --;  
Line 29, "b (Slavename)" should start on a new line; and  
Line 65, "laneonbData[31:0]" should read -- lane on bData[31:0] --.

COLUMN 19:

Line 10, "bBurst L" should read -- bBurst\_L --; and  
Line 14, "bBurs tAck\_L" should read -- bBurstAck\_L --.

COLUMN 36:

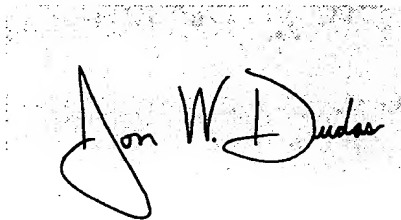
Line 36, "bus IO" should read -- bus/IO --.

COLUMN 37:

Line 24, "Gbus IO bus" should read -- G bus/IO bus --; and  
Line 27, "G bus IO" should read -- G bus/IO --.

Signed and Sealed this

Twenty-third Day of January, 2007

A handwritten signature in black ink, reading "Jon W. Dudas", is written over a rectangular, textured grey background.

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*